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Examiner Signature	/Zheng Wei/	Date Considered	01/06/2009
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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /ZW/

PTO/SB/06b (09-08)

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Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Application Number	10/732,764-Conf. #6964
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		Art Unit	2192
		Examiner Name	Z. Wei
Sheet	2	of	2
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	Search Report from United Kingdom Patent Application No. PCT/ GB2004/001744 dated November 10, 2004.	
	CB	Search Report from United Kingdom Patent Application No. PCT/GB2004/001722 dated January 26, 2005.	
	CC	Mathew J. Hostetter, Clifford T. Matthews, "Executor Internals: How to Efficiently Run Mac Programs on PCs" Online! 1996, XP002300303 Retrieved from the Internet: URL: http://www.ardi.com/MacHack_96.html > retrieved on 2004-10-12! Page 3, paragraph 4 - paragraph 6	
	CD	"Program of MacHack '96 (confirming the publication date of document D1) "Online! XP002300340, Retrieved from the Internet: URL: www.machack.com/96/Sessions.html > retrieved on 2004-10-12	
	CE	Palaniswamy A C et al: "An efficient implementation of lazy reevaluation" Proceedings. 25 th Annual Simulation symposium (Cat. No. 92TH0433-3) IEEE Comput. Soc. Press Los Alamitos, CA, USA, 1992, pages 140-146, XP010028446, ISBN: 0-8186-2765-4	
	CF	David Ung and Cristina Cifuentes: "Machine-Adaptable Dynamic Binary Translation" Proceedings of the ACM Sigplan Workshop on Dynamic and Adaptive Compilations and Optimization, XX, XX, January 2000 (2000-01), pages 41-51, XP002279759	
	CG	Cifuentes C et al: "The design of a resourceable and retargetable binary translator: Reverse Engineering, 1999 Proceedings. Sixth Working Conference on Atlanta, GA, USA 6-8 OCT. 1999, Los Alamitos, CA, USA, IEEE Comput. Soc, US 6 October 1999 (1999-10-06), pages 280-291, XP010360228, ISBN: 0-7695-0303-9	
	CH	Knoop J et al: "Partial Dead Code Elimination" ACM Sigplan Notices, Association for Computing Machinery, New York, US, vol., 29, no. 6, 1 June 1994, pages 147-158, XP000450413, ISSN: 0362-1340	

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